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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/782,448	02/13/2001	Justin H. Benson	230074-0235	9877
7:	590 04/06/2005		EXAMINER	
Ted R. Rittmaster			LEVI, DAMEON E	
FOLEY & LAI	RDNER			
Suite 3500			ART UNIT	PAPER NUMBER
2029 Century P	Park East	2841		
Los Angeles, (CA 90067-3021	DATE MAILED: 04/06/2005		

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary		Application No.	Applicant(s)			
		09/782,448	BENSON ET AL.			
		Examiner	Art Unit			
_		Dameon E. Levi	2841			
Period fo	The MAILING DATE of this communication apport	pears on the cover sheet with the c	orrespondence address			
THE - External enternal ente	ORTENED STATUTORY PERIOD FOR REPL MAILING DATE OF THIS COMMUNICATION. Insions of time may be available under the provisions of 37 CFR 1.1 SIX (6) MONTHS from the mailing date of this communication. In period for reply specified above is less than thirty (30) days, a repl or period for reply is specified above, the maximum statutory period or to reply within the set or extended period for reply will, by statute reply received by the Office later than three months after the mailing and patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be time y within the statutory minimum of thirty (30) days will apply and will expire SIX (6) MONTHS from a cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).			
Status						
1)⊠	Responsive to communication(s) filed on <u>02/1</u> .	3/2001 New Application.				
2a)□						
3)□	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Dispositi	ion of Claims					
5)⊠ 6)⊠ 7)⊠	Claim(s) 1-17 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration. Claim(s) 12-17 is/are allowed. Claim(s) 1-8 is/are rejected. Claim(s) 9-11 is/are objected to. Claim(s) are subject to restriction and/or election requirement.					
Applicati	on Papers					
10)	The specification is objected to by the Examine The drawing(s) filed on is/are: a) acc Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct The oath or declaration is objected to by the Ex	epted or b) objected to by the Education of the Education is required if the drawing(s) is objected to be supported in the Education of the Ed	e 37 CFR 1.85(a). ected to. See 37 CFR 1.121(d).			
Priority u	ınder 35 U.S.C. § 119					
a)[Acknowledgment is made of a claim for foreign All b) Some * c) None of: 1. Certified copies of the priority document 2. Certified copies of the priority document 3. Copies of the certified copies of the priority document application from the International Bureausee the attached detailed Office action for a list	s have been received. s have been received in Application rity documents have been receive u (PCT Rule 17.2(a)).	on No ed in this National Stage			
	e of References Cited (PTO-892)	4) 🔲 Interview Summary				
3) 🔯 Inform	e of Draftsperson's Patent Drawing Review (PTO-948) nation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) r No(s)/Mail Date	Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:	atent Application (PTO-152)			

DETAILED ACTION

Election/Restrictions

Applicant's election with traverse of claims 1-17 in the reply filed on 01/21/2005 is acknowledged. The traversal is on the ground(s) that that the security module and the method of making the security module are so interrelated and specific to one another that they should be examined together and included in a single patent. This is not found persuasive because the manufacturing process as claimed can be used to make other materially different products.

The requirement is still deemed proper and is therefore made FINAL.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-8 are rejected under 35 U.S.C. 102(b) as being anticipated by Kleijne US Patent 4593384.

Regarding claim 1, Kleijne discloses an apparatus comprising:

a substrate (for example, see element P6, Figs 1-8) composed of a plurality of layers including a first layer, the first layer of the substrate for supporting circuit components to be protected (for example, see element 19, Figs 1-8),

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a cover member composed of a plurality of layers(for example, see element P1-P5, Figs 1-8, see column 4, lines 13-23), the cover member having a surface for abutting the first layer of the substrate, the cover member defining an enclosure space for enclosing circuit components to be protected between the cover member and the substrate when the circuit components are supported on the substrate and the cover member is abutted to the substrate(for example, see column 2, lines 48-55)

and

a sensor (for example, see column 2, lines 55-65, see Figs 1-8)comprising at least one conduction path disposed in at least one of the layers below the first layer of the substrate and at least one conduction path disposed in at least one of the layers of the cover member.

Regarding claim 2, Kleijne discloses further comprising an electronic circuit disposed on the substrate for detecting at least one of a short or a break in the conduction path disposed in the substrate or in the cover member(see Abstract).

Regarding claim 3, Kleijne discloses wherein the conduction path in the substrate and the cover member comprise at least one serpentine path(for example, see Figs 6A-7B).

Regarding claim 4, Kleijne discloses wherein the conduction path in the substrate and the cover member comprises a plurality of serpentine paths(for example, see Figs 6A-7B).

Regarding claim 5, Kleijne discloses wherein the substrate and the cover member comprise a plurality of layers, each layer having at least one conduction path(for example, see column 4, lines 13-23, see elements 21,23,24, Figs 3A – 3C2).

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Regarding claim 6, Kleijne discloses wherein each layer of the substrate and the cover member comprises a plurality of serpentine paths(for example, see Figs 6A-7B). **Regarding claim** 7, Kleijne et al discloses wherein the plurality of serpentine paths are disposed on the substrate and the cover member in a pseudo-random configuration(for example, see Figs 6A-7B).

Regarding claim 8, Kleijne discloses each conduction path comprising a thin-film conductor formed directly on the associated layer(for example, see elements 21,23,24, Figs 3A1, 3B2, 3C2).

Allowable Subject Matter

Claims 12-17 are allowable.

The following is a statement of reasons for the indication of allowable subject matter: **Regarding claims** 12-13, patentability exists, at least in part with the inclusion of a sensor comprising a plurality of vias in the side portions of the cover member, each via comprising an electrically conductive material defining a plurality of conduction paths extending transverse to the first and second surfaces, surrounding the enclosure space, when the second surface of the cover member is abutted to the first surface of the substrate.

Regarding claims 14-17, patentability exists, at least in part, with a sensor comprising a plurality of solder balls electrically connected to the second surface of the cover member, the plurality of solder balls electrically and mechanically connecting to the first surface of the substrate when the second surface of the cover member is abutted to the first surface of the substrate.

The prior art does not teach or suggest sensors comprising vias, or solder balls employed in the manner as taught by the Applicants.

In addition, claims 9-11 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dameon E. Levi whose telephone number is (571) 272-2105. The examiner can normally be reached on Mon.-Fri. (9:00 - 5:00).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kamand Cuneo can be reached on (571) 272-1957. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Dameon E Levi Examiner Art Unit 2841

DEL

K.CUNED SUPERVISORY PATENT EXAMINER